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FIG. 1

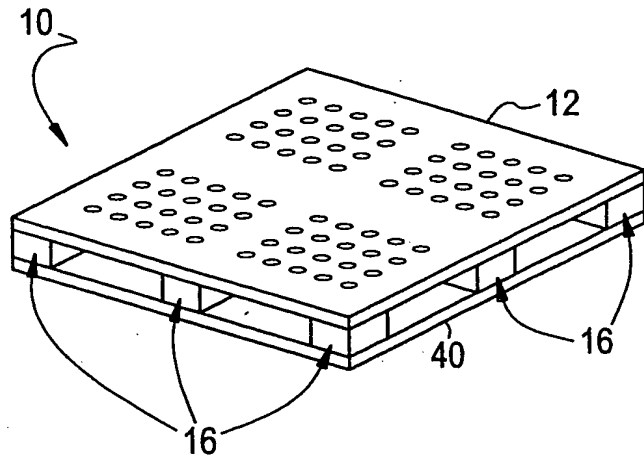
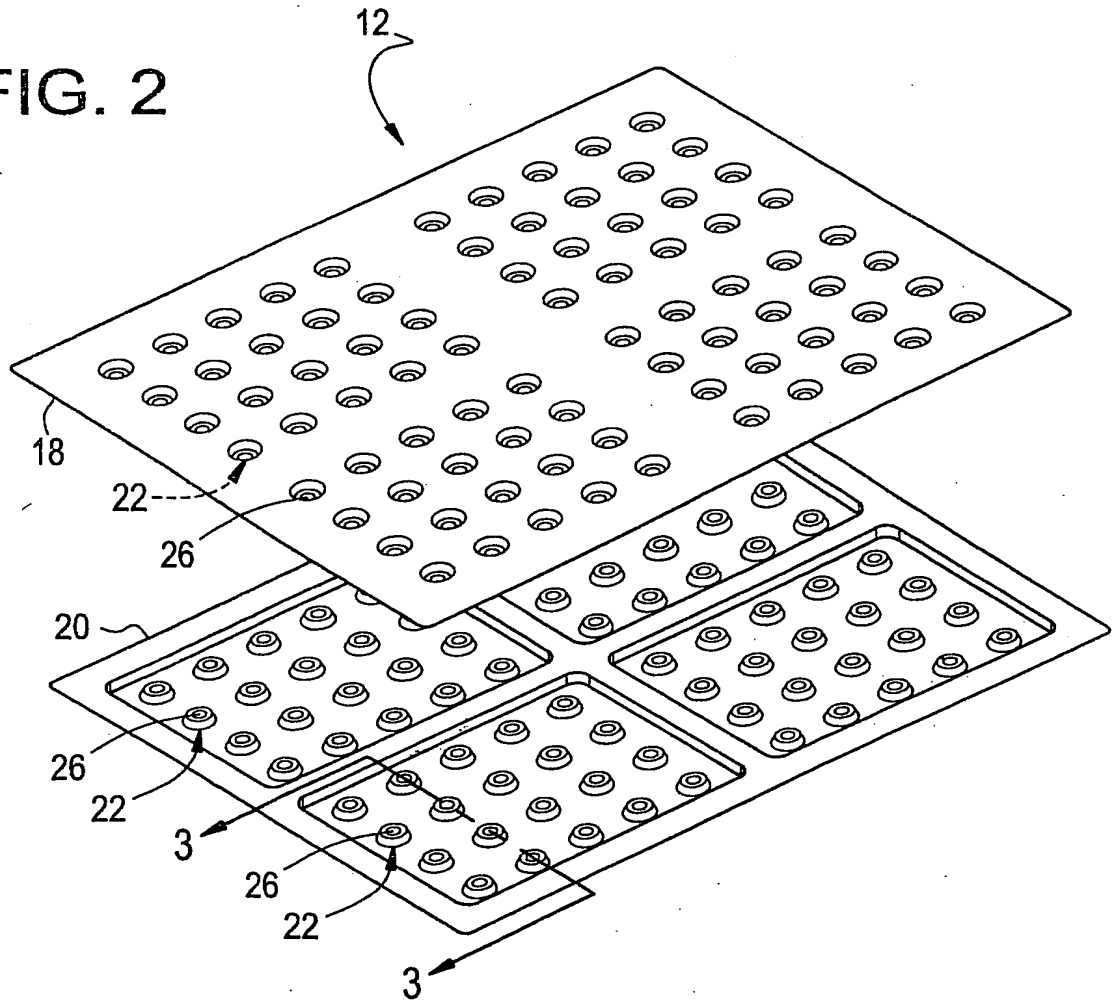


FIG. 2



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FIG. 3

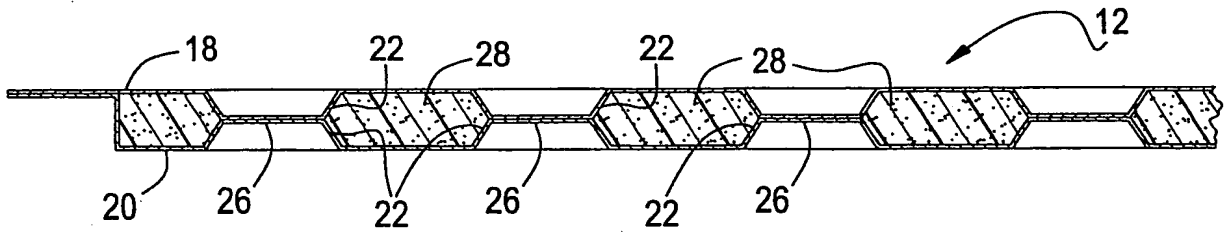


FIG. 4A

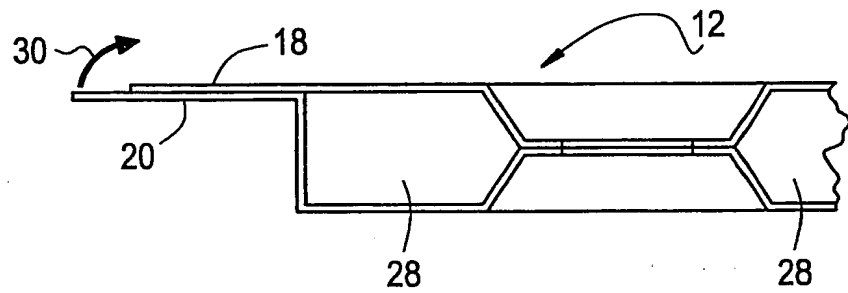


FIG. 4B

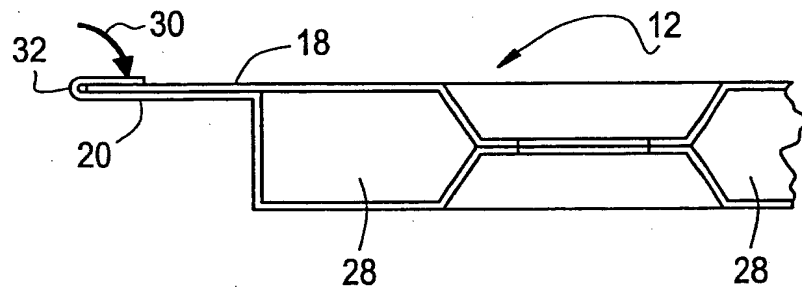


FIG. 4C

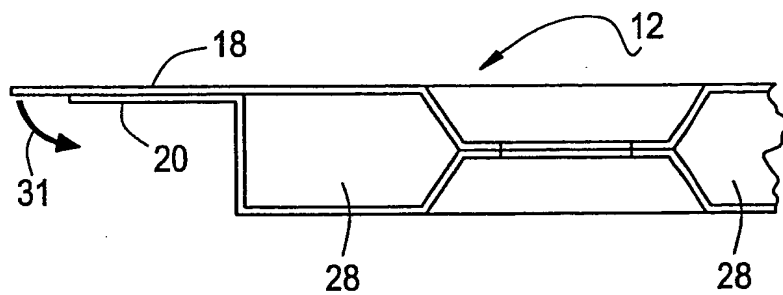
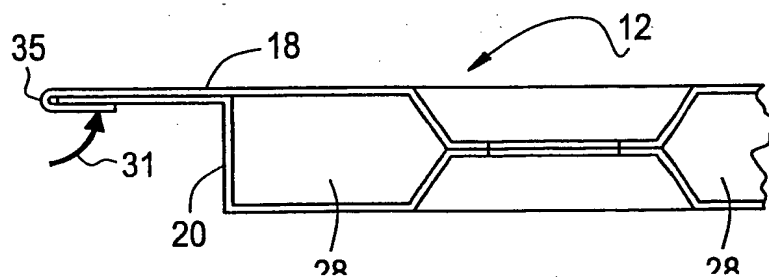


FIG. 4D



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FIG. 4E

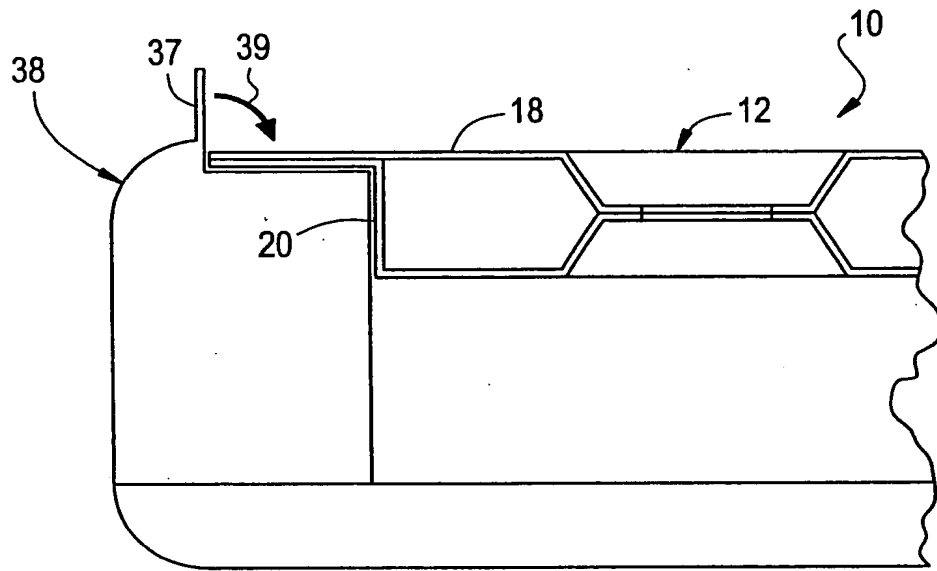
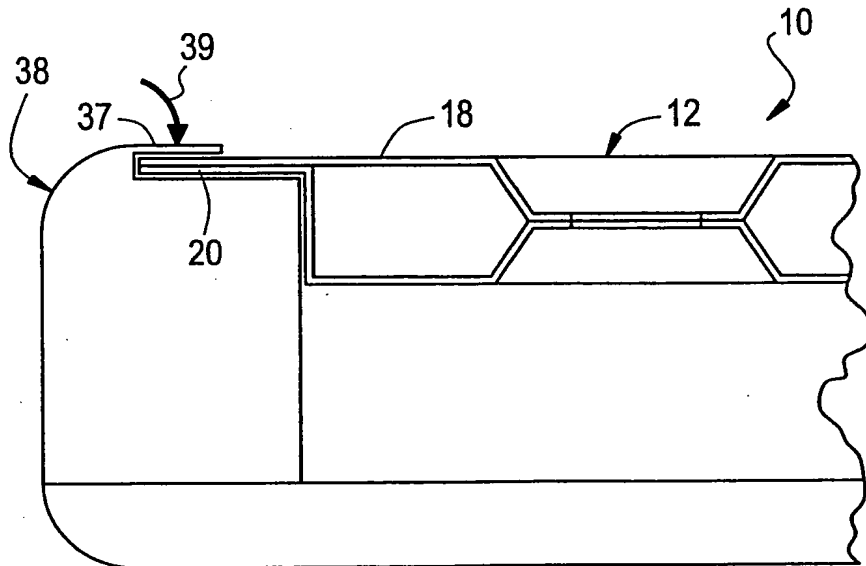
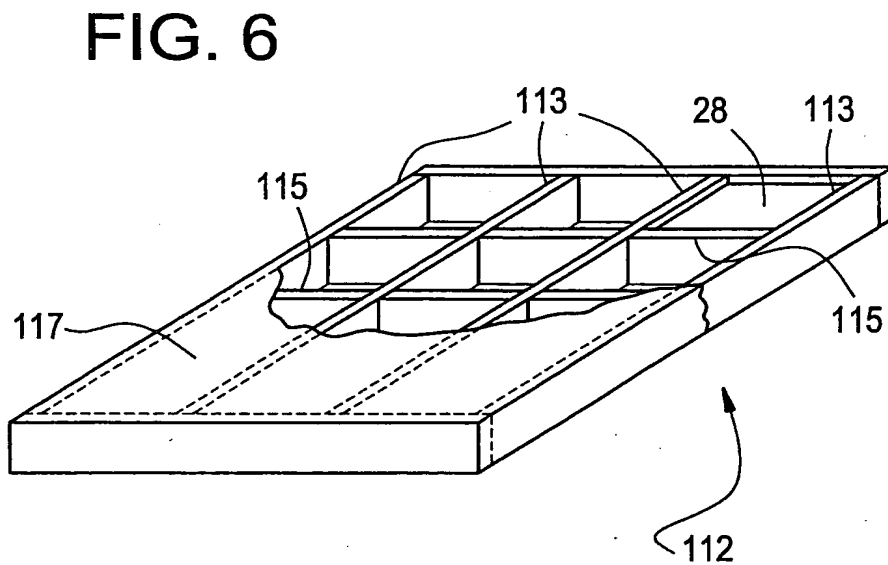
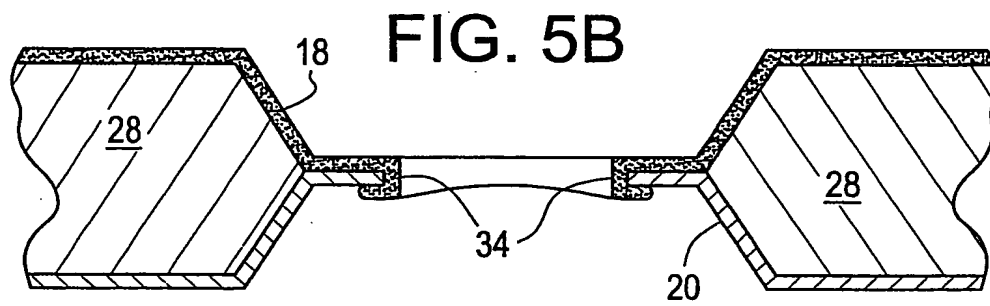
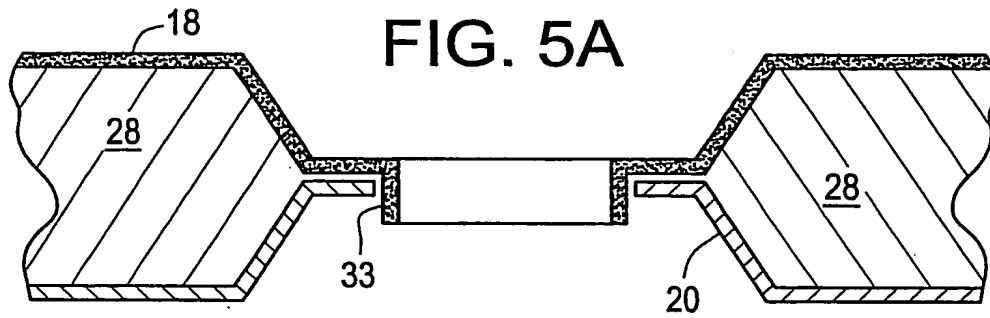


FIG. 4F

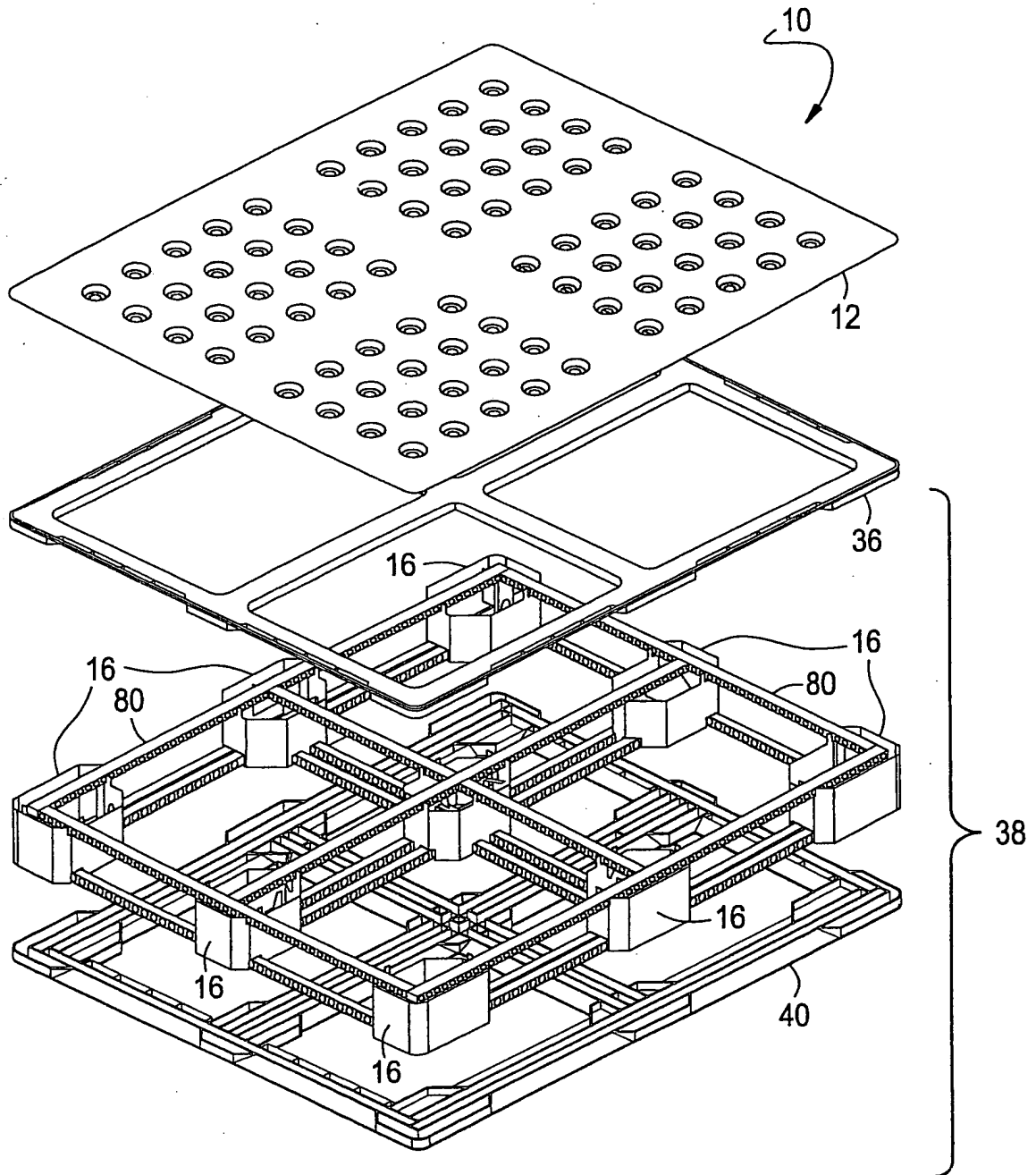


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FIG. 7



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FIG. 8A

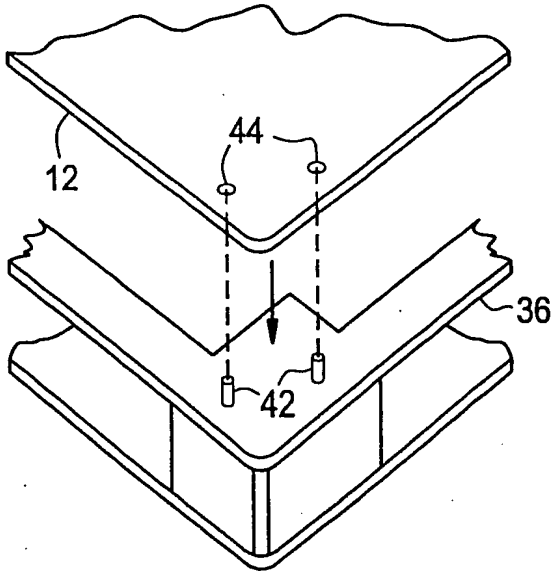


FIG. 8B

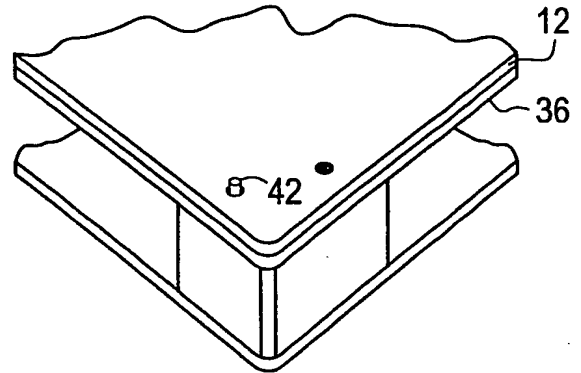


FIG. 8C

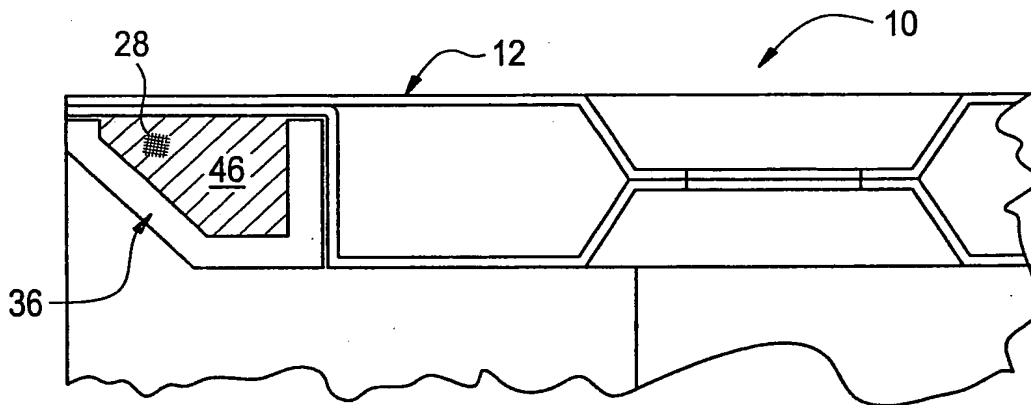
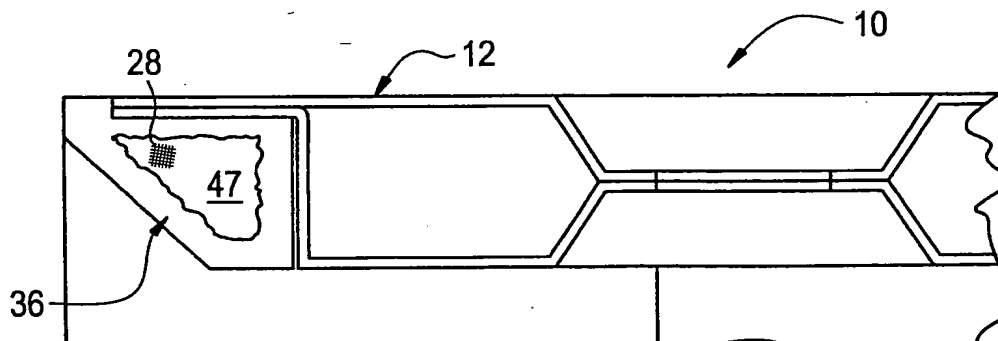
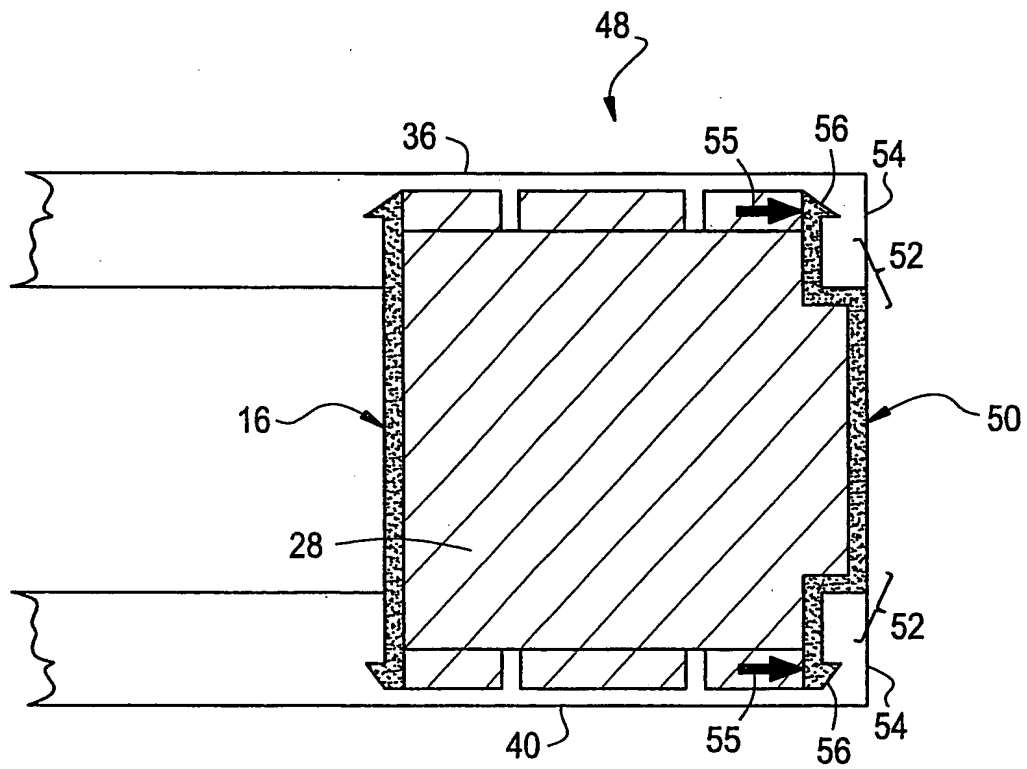


FIG. 8D



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FIG. 9



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FIG. 10A

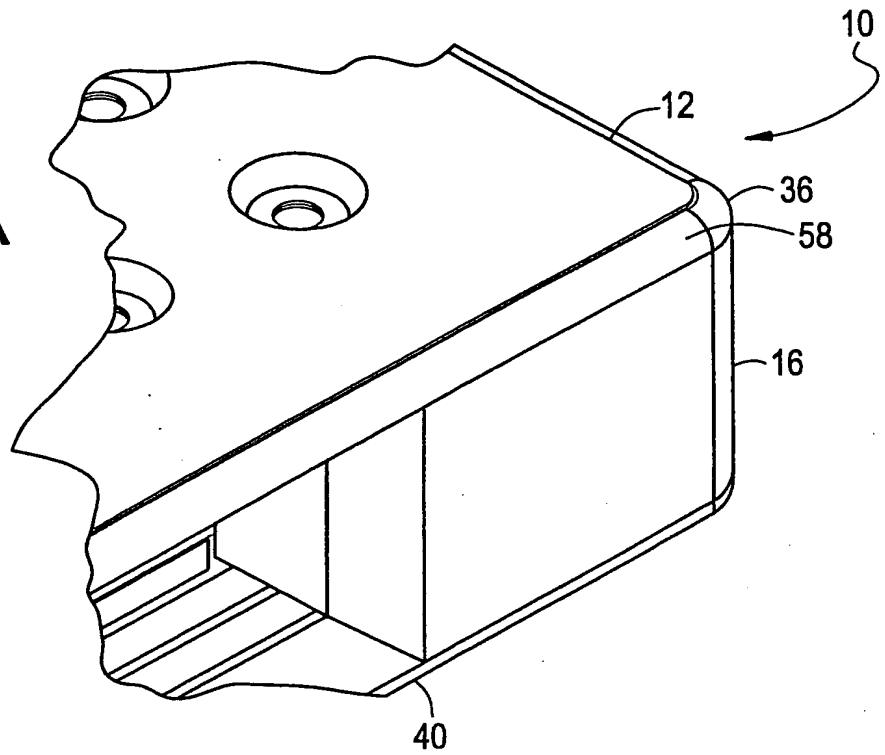
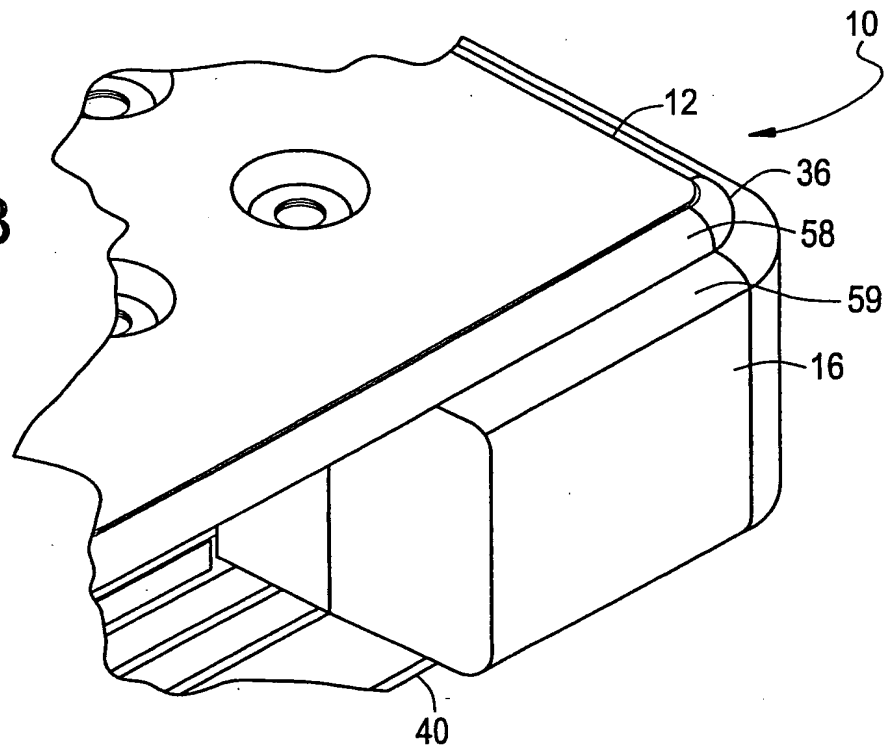


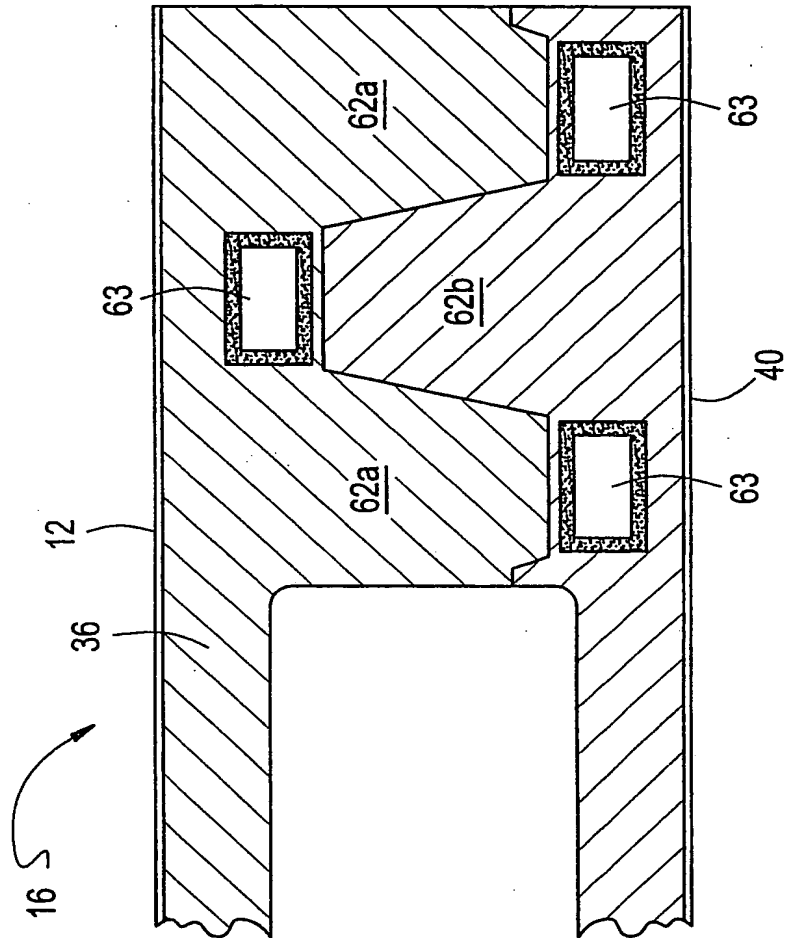
FIG. 10B





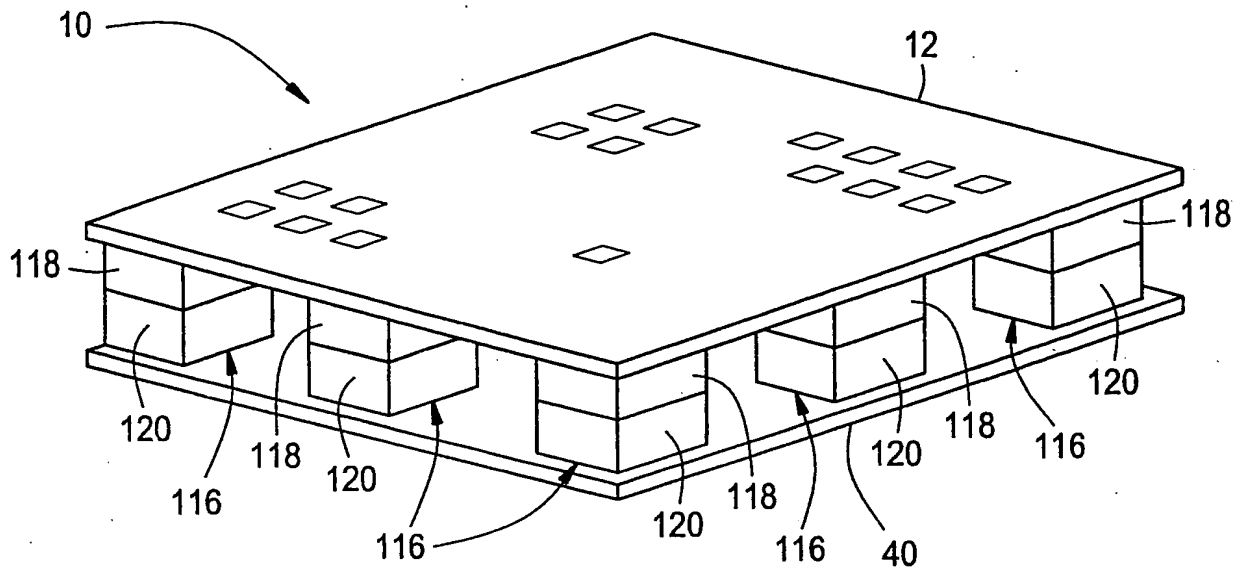
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FIG. 11



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FIG. 12A



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FIG. 12B

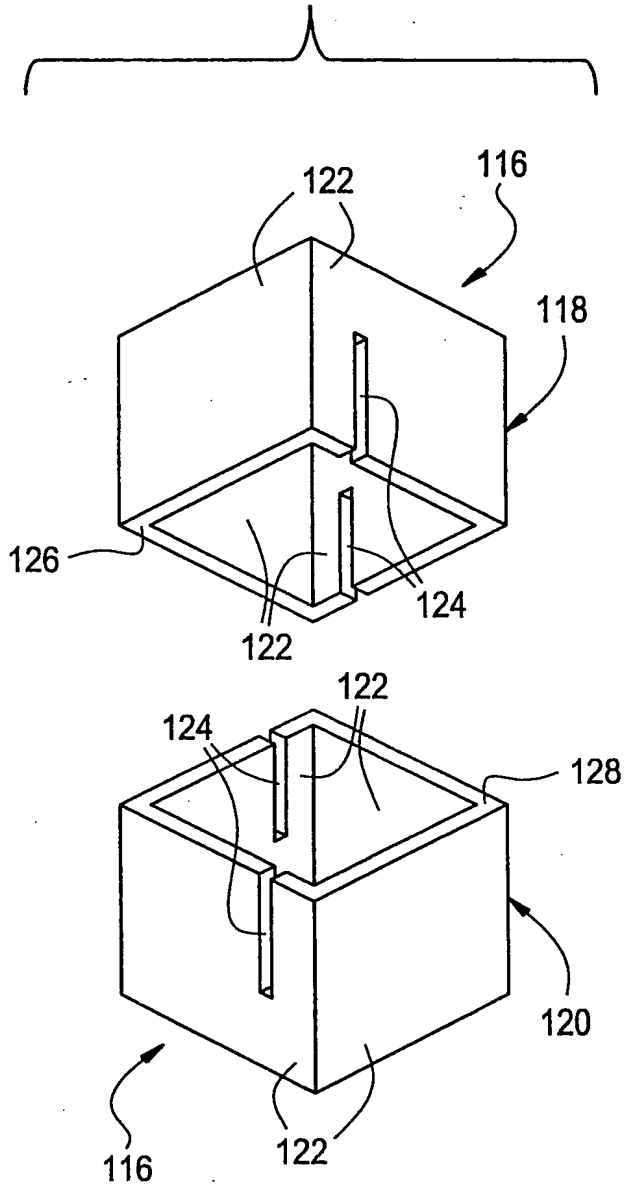


FIG. 12C

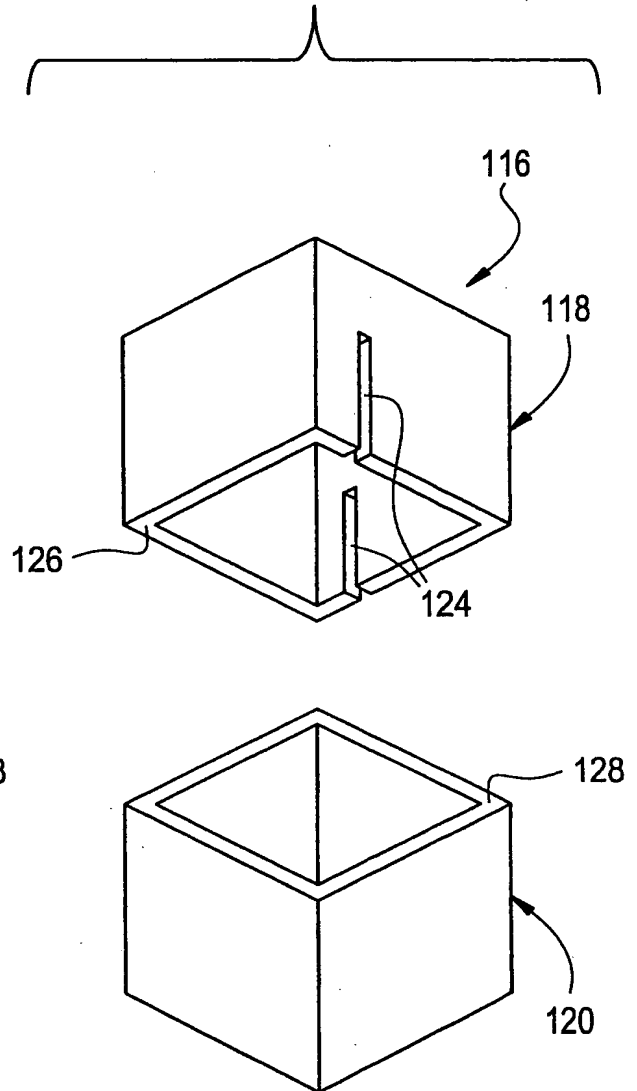


FIG. 13A

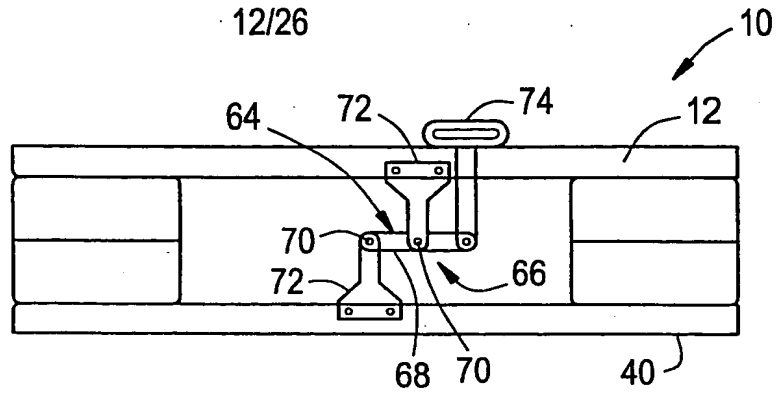


FIG. 13B

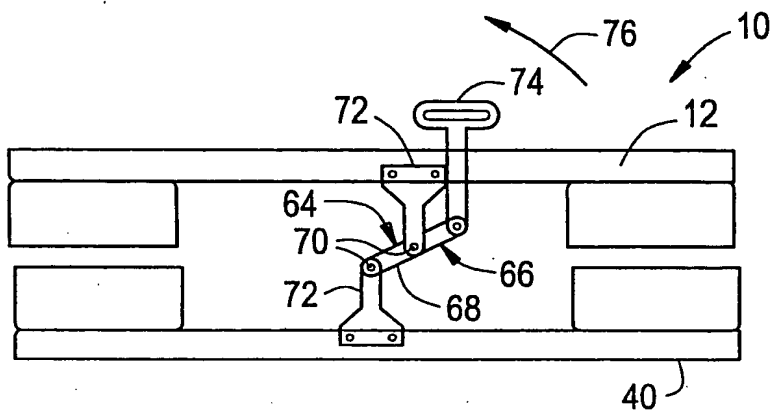


FIG. 13C

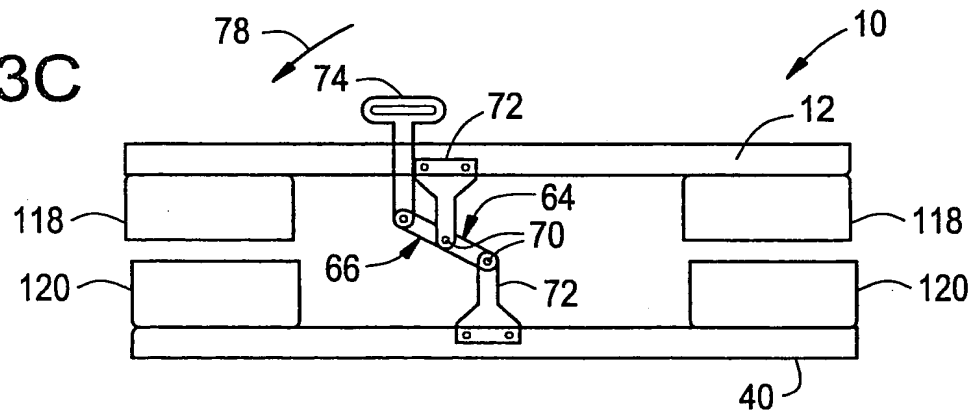
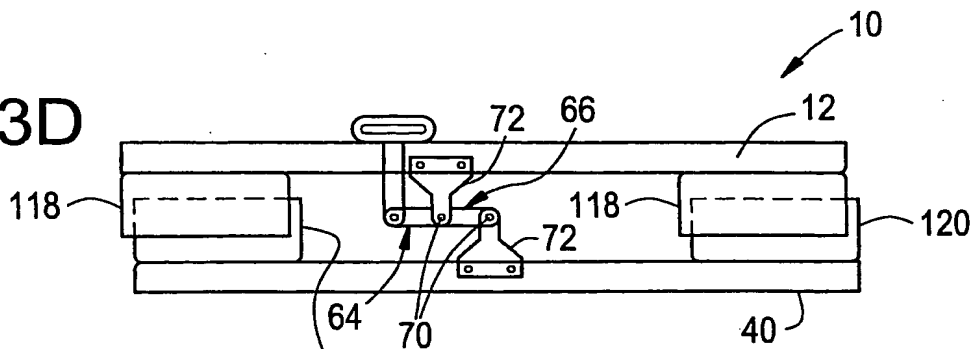
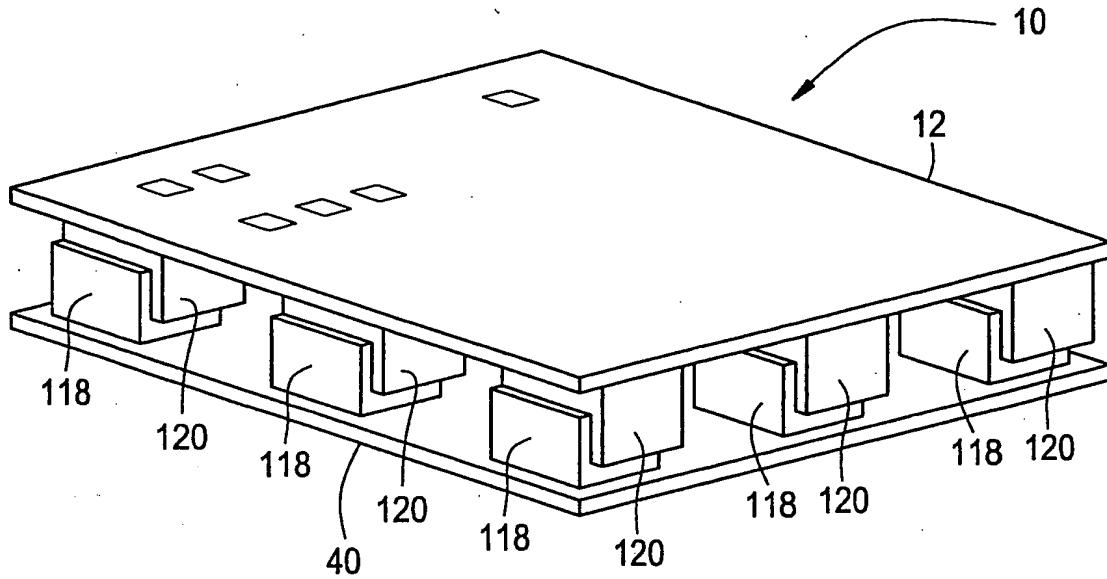


FIG. 13D



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FIG. 13E



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FIG. 14

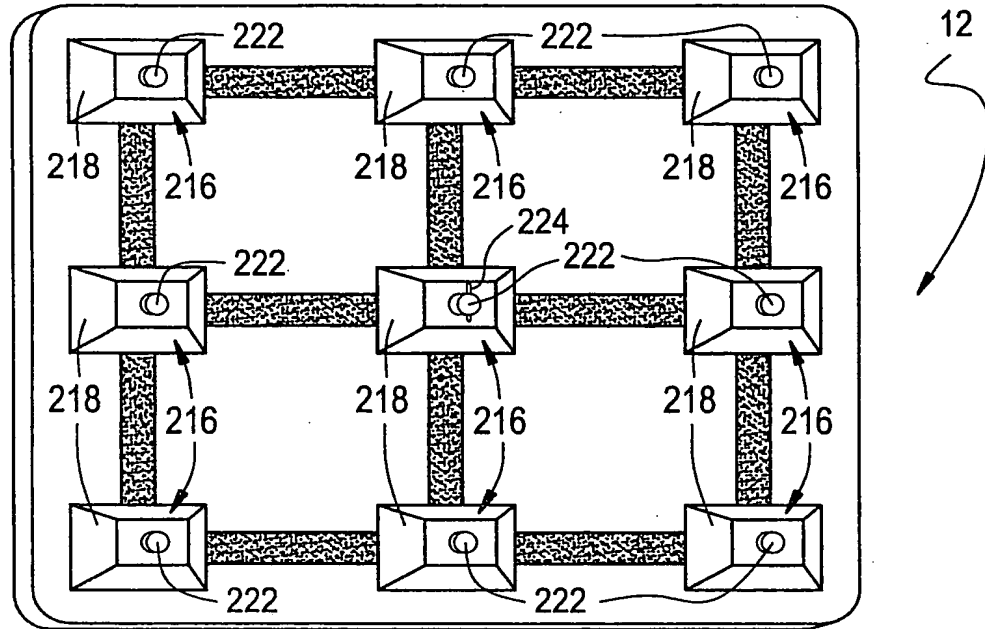
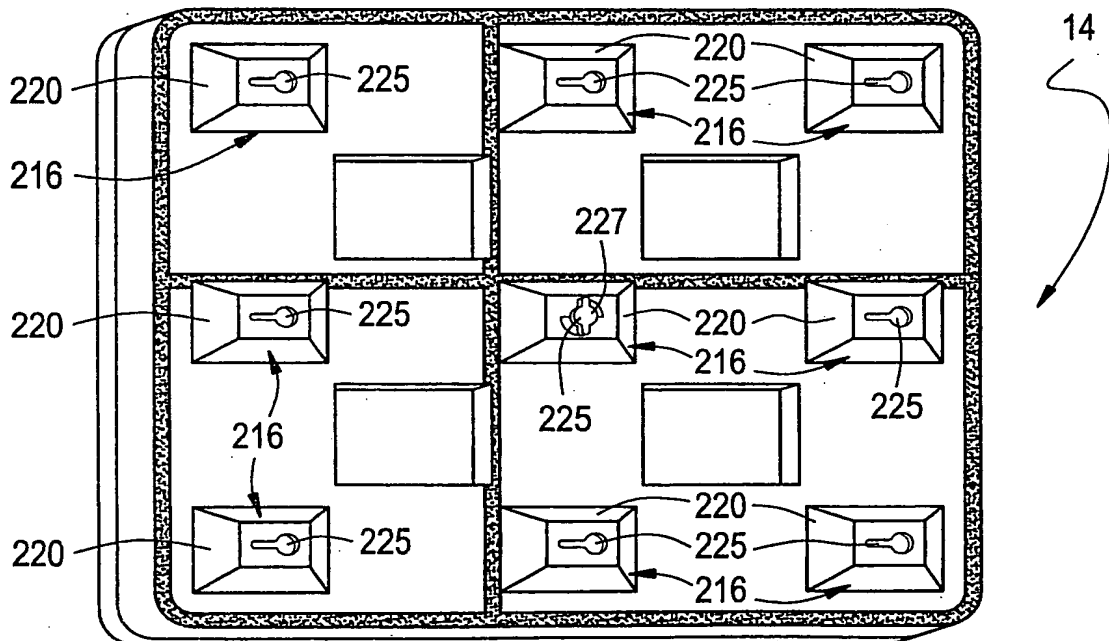


FIG. 15



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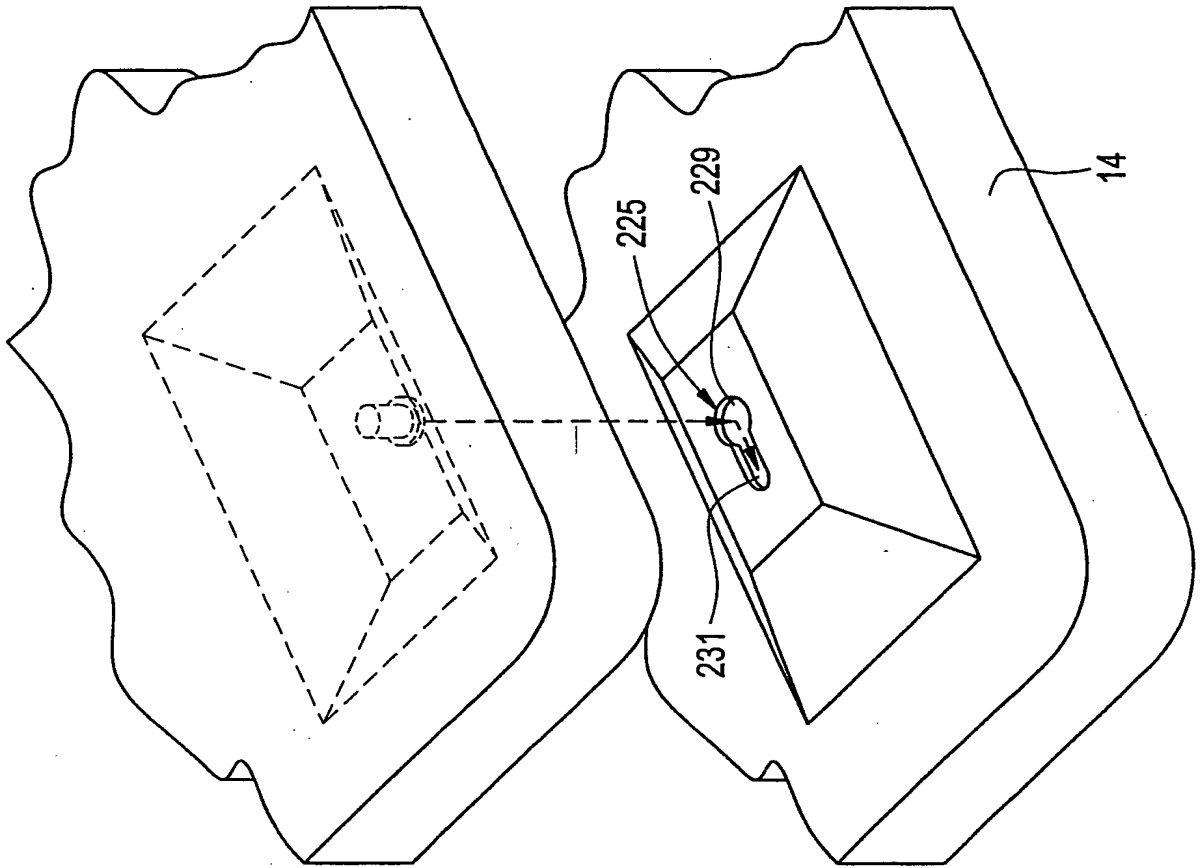


FIG. 16

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FIG. 17

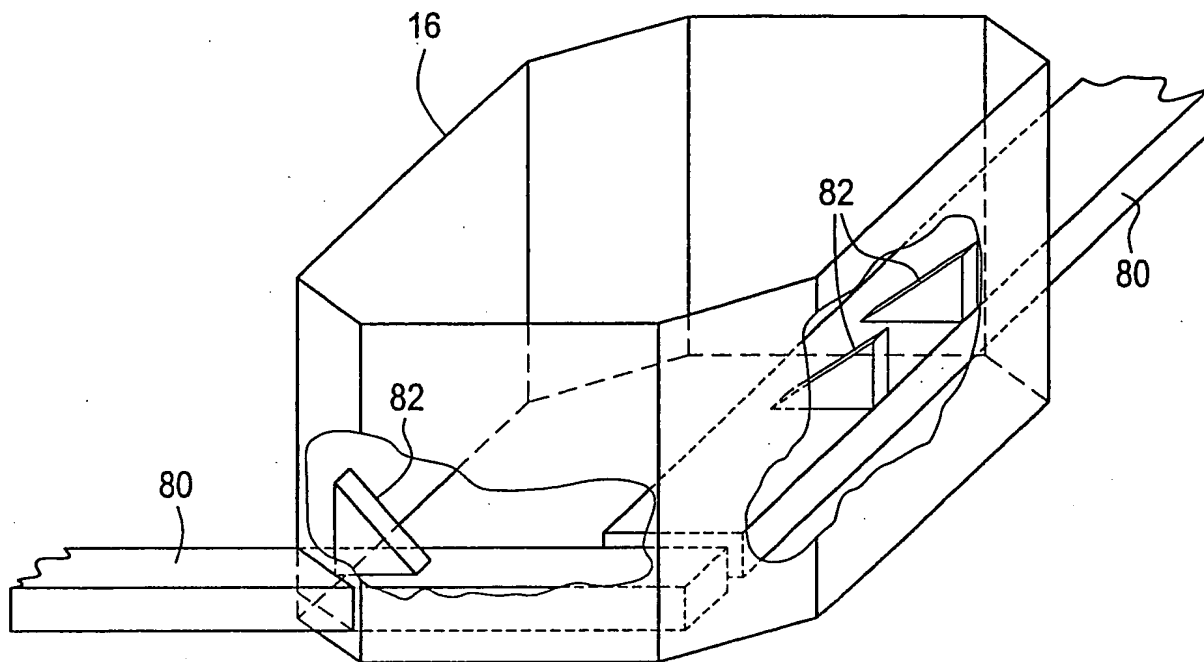
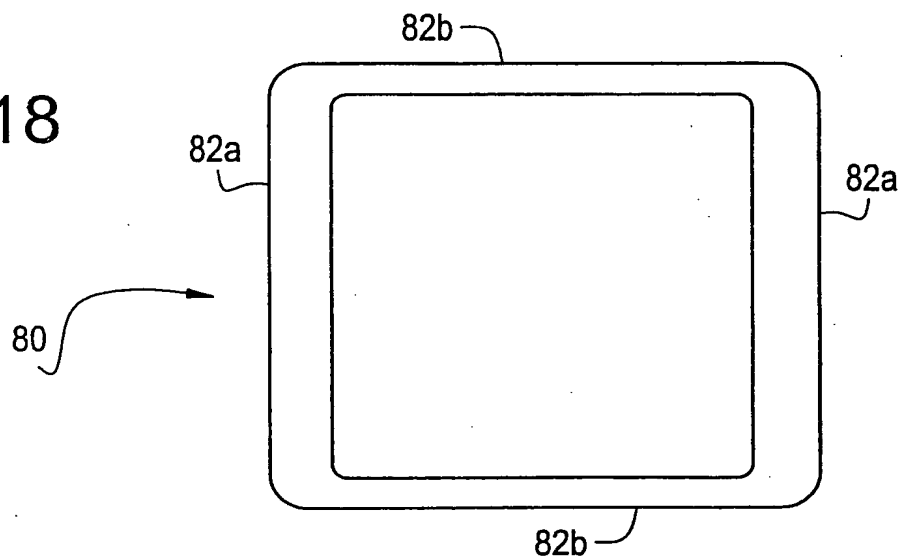


FIG. 18





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FIG. 19A

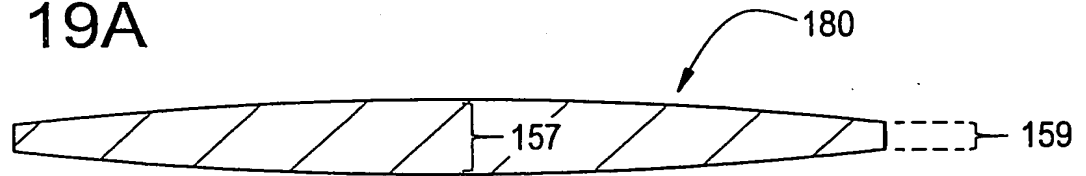


FIG. 19B

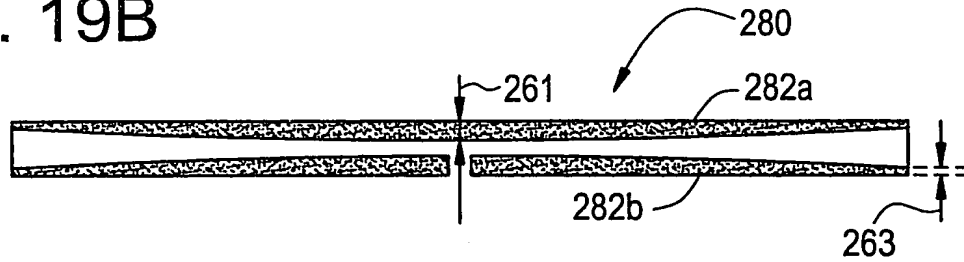


FIG. 20

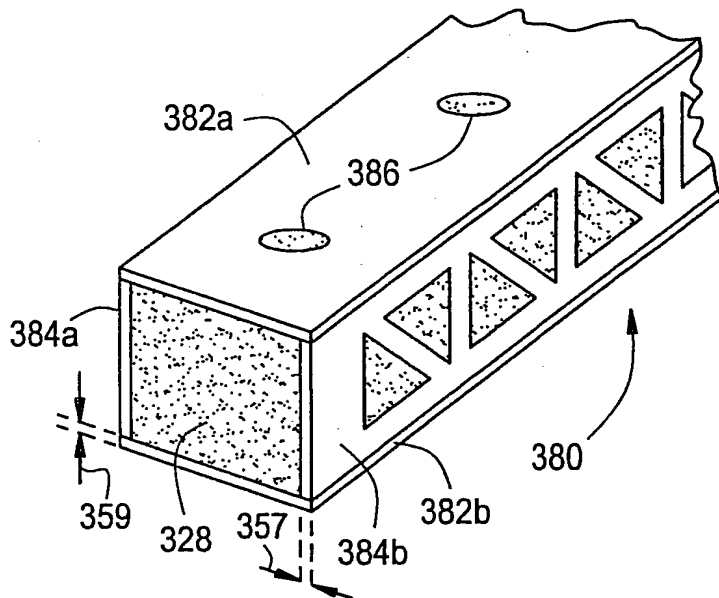
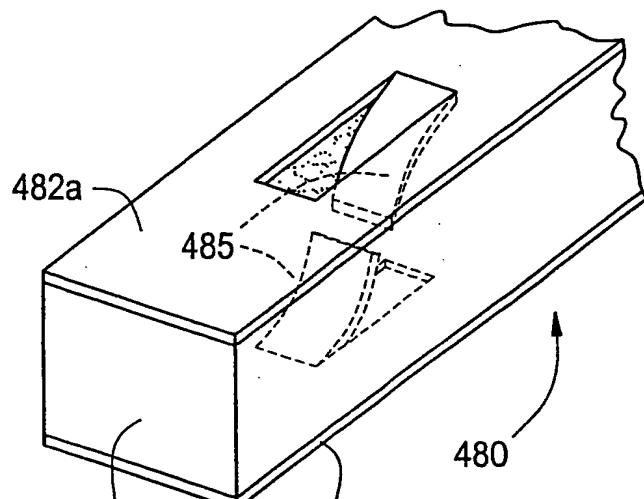


FIG. 21



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FIG. 22

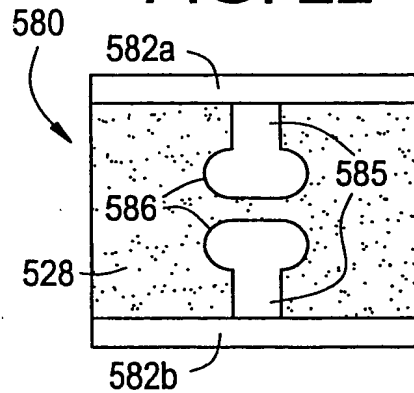
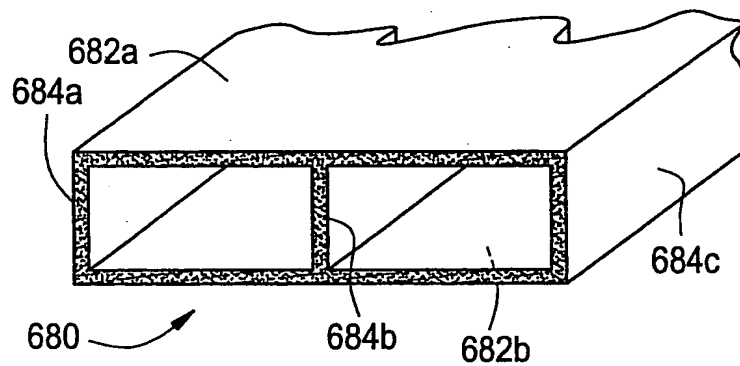


FIG. 23



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FIG. 24A

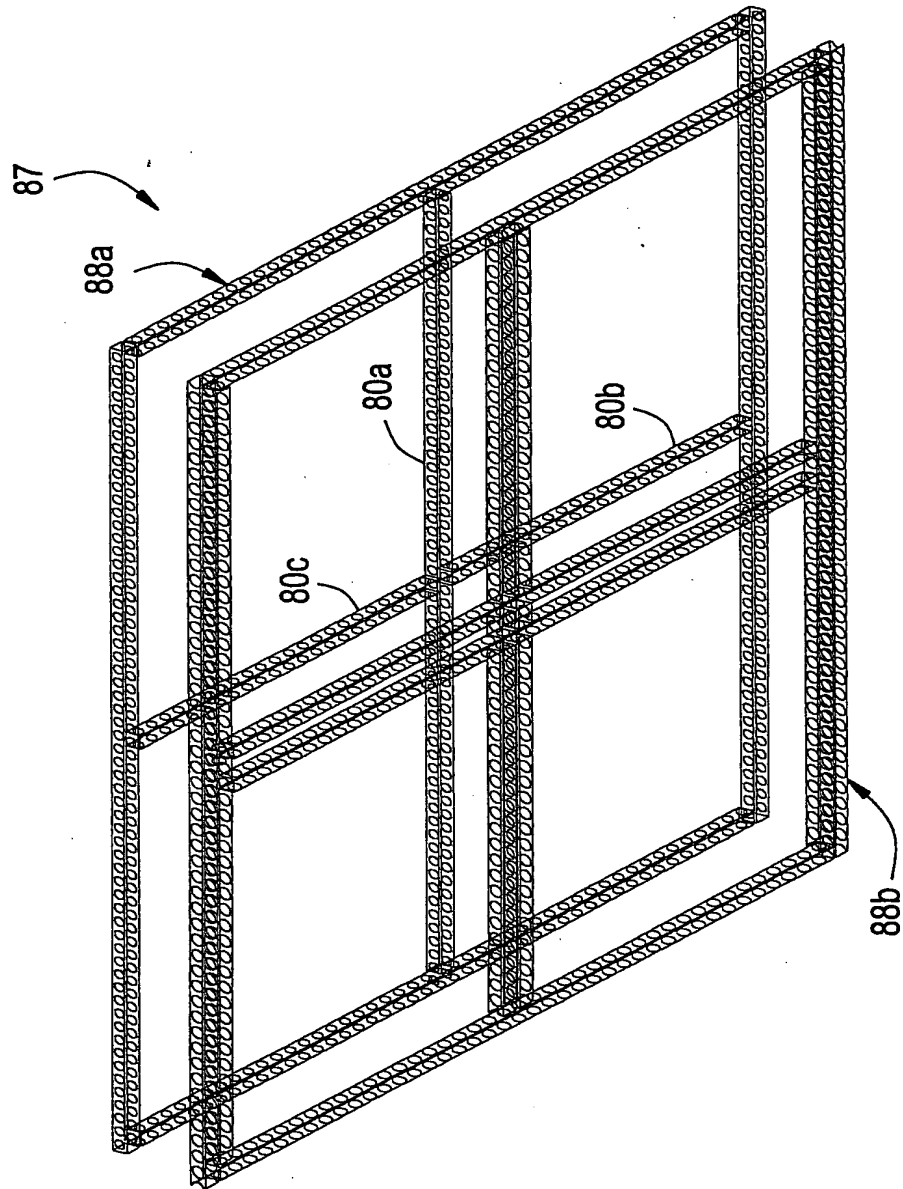


FIG. 24C

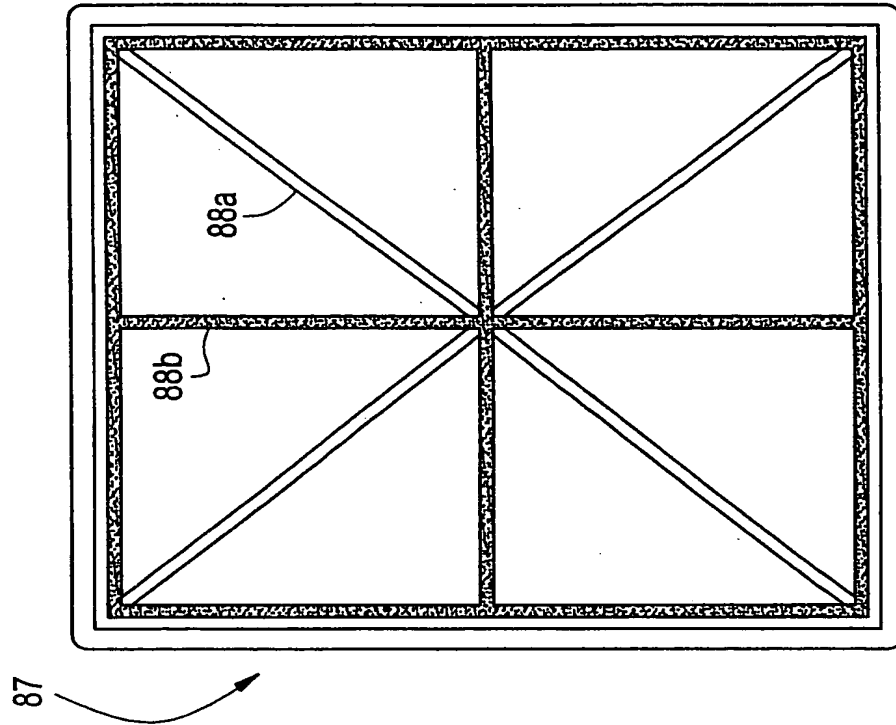


FIG. 24B

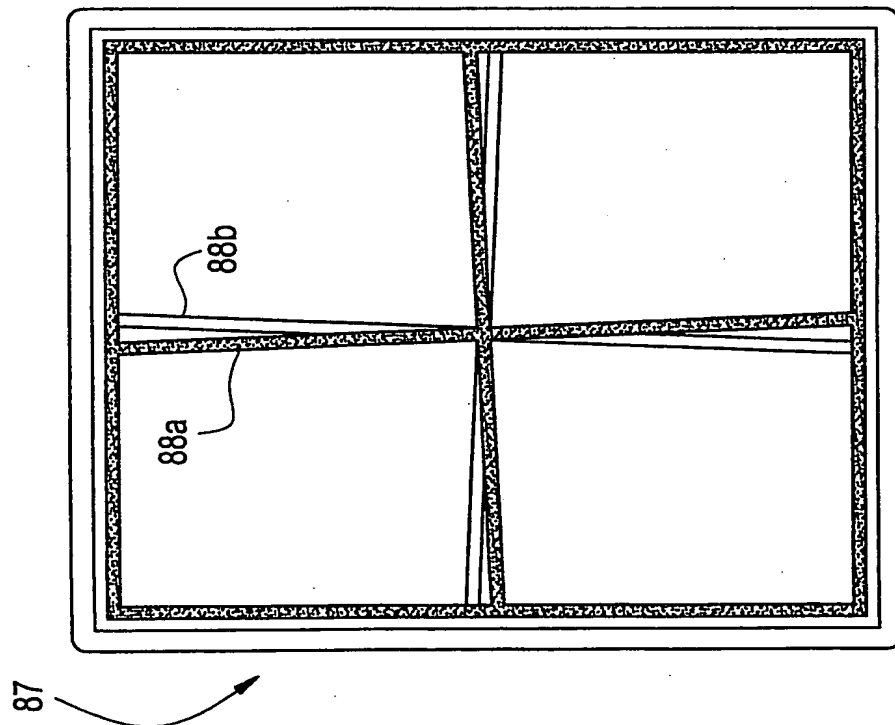
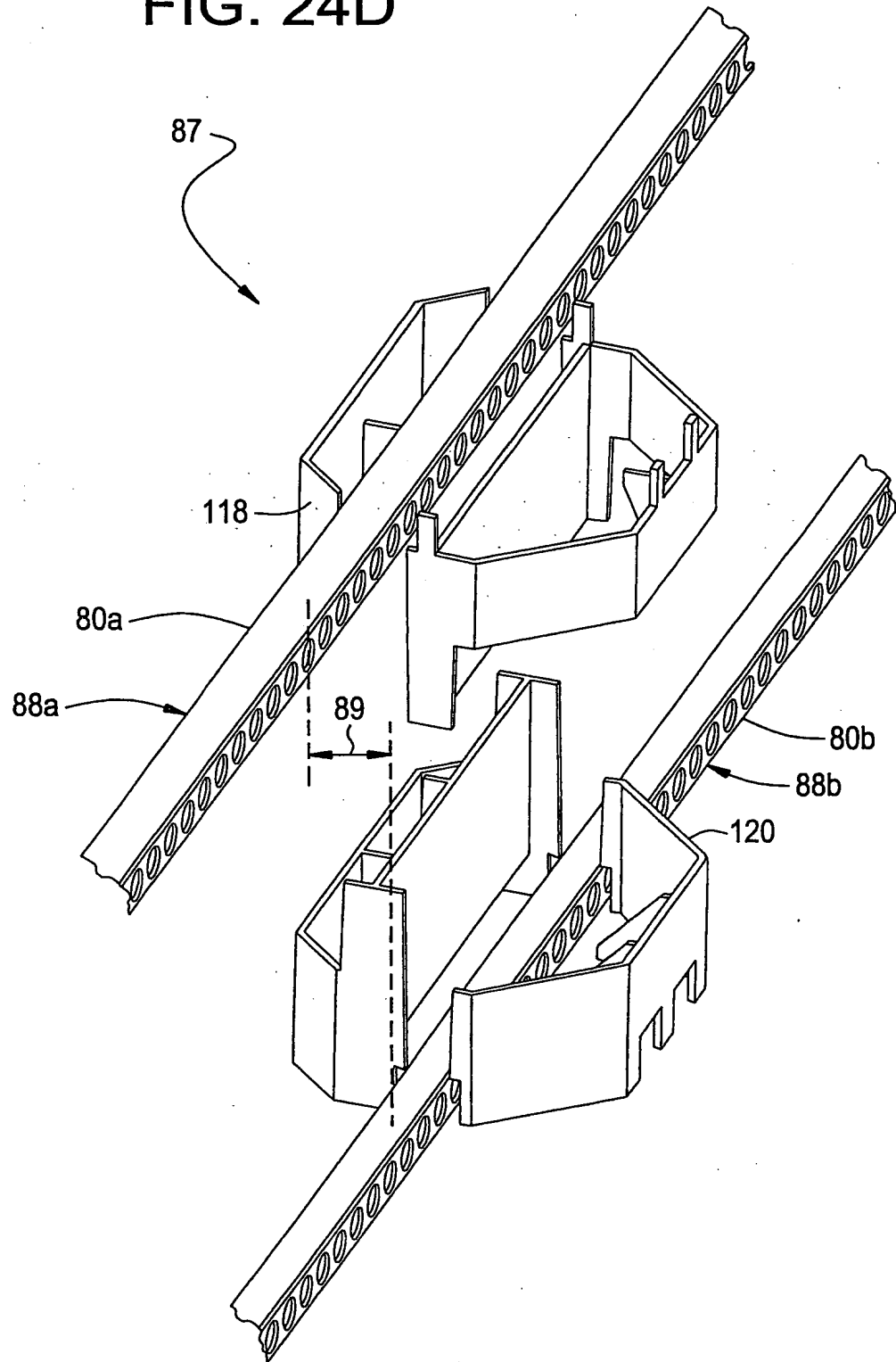
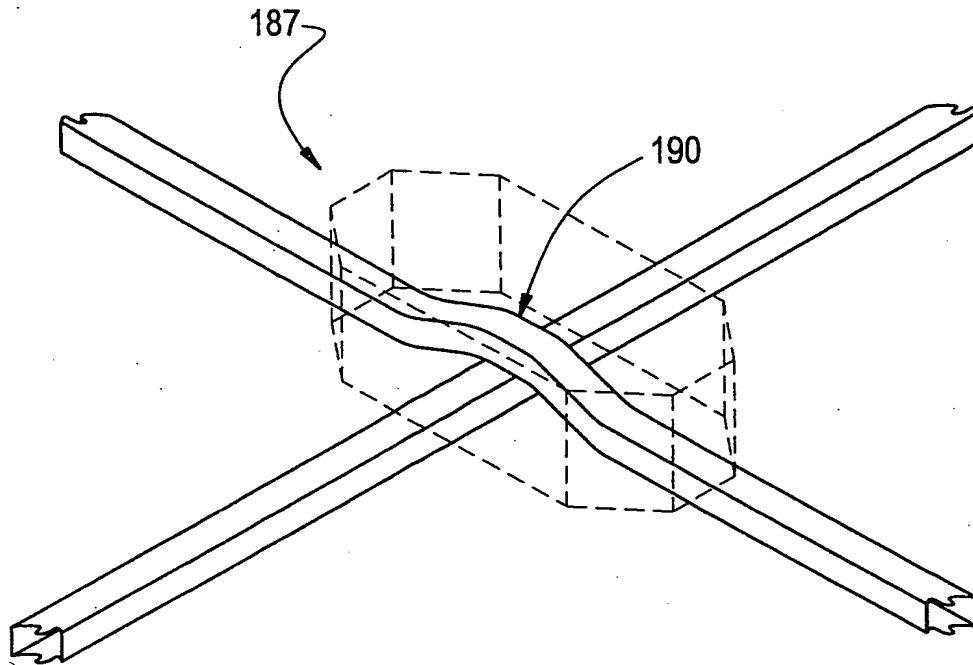


FIG. 24D



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FIG. 25



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FIG. 26A

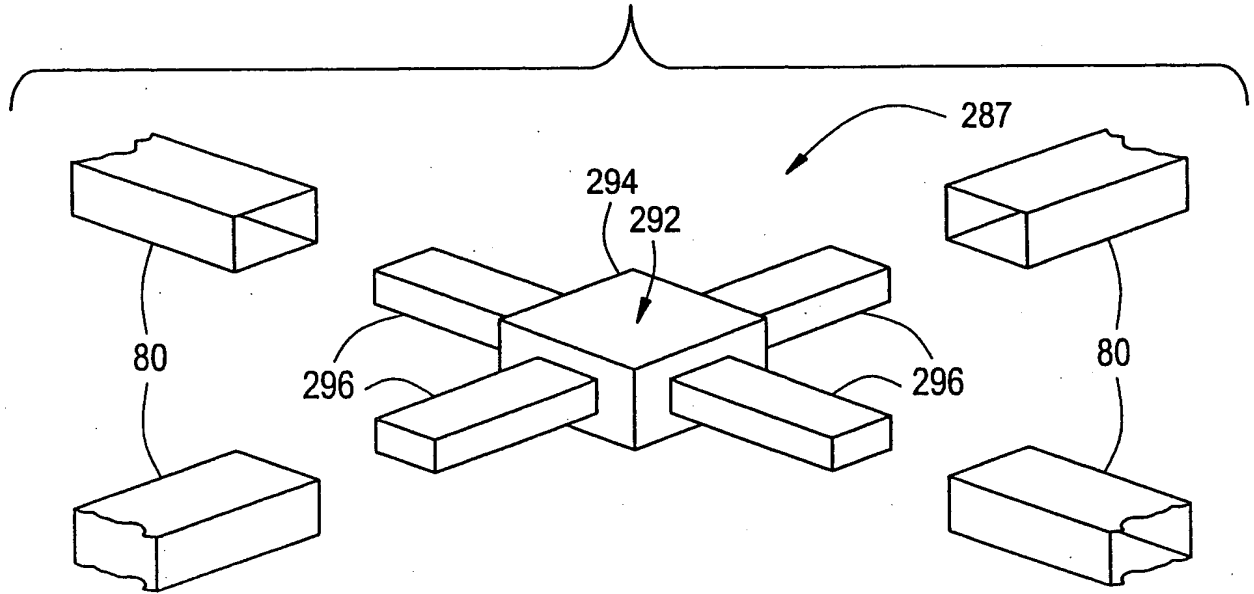
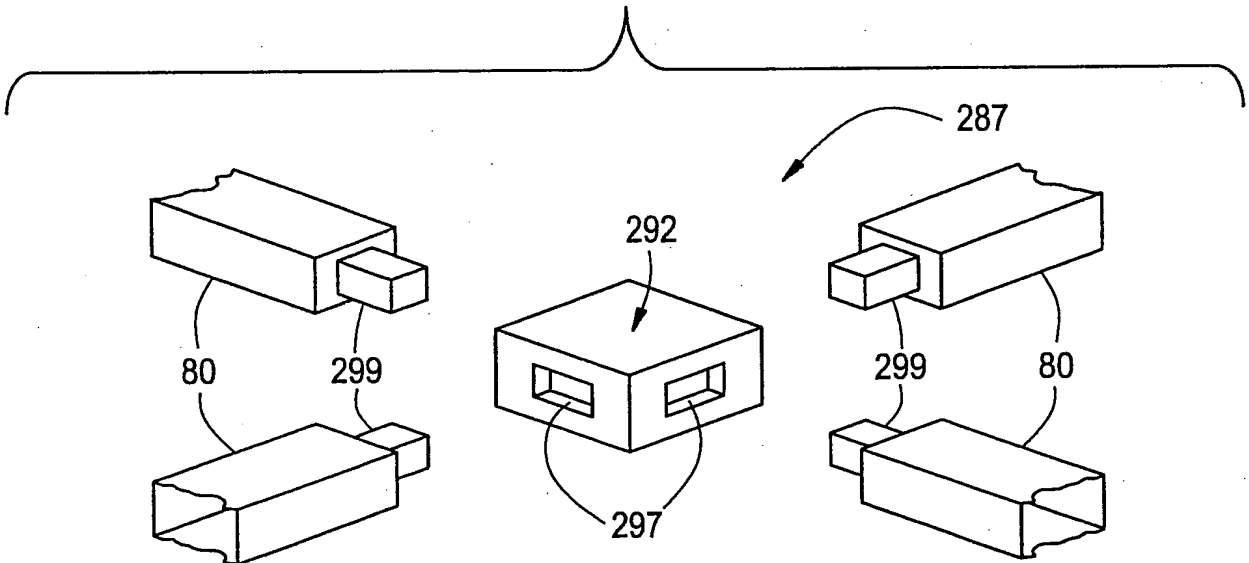


FIG. 26B



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FIG. 26C

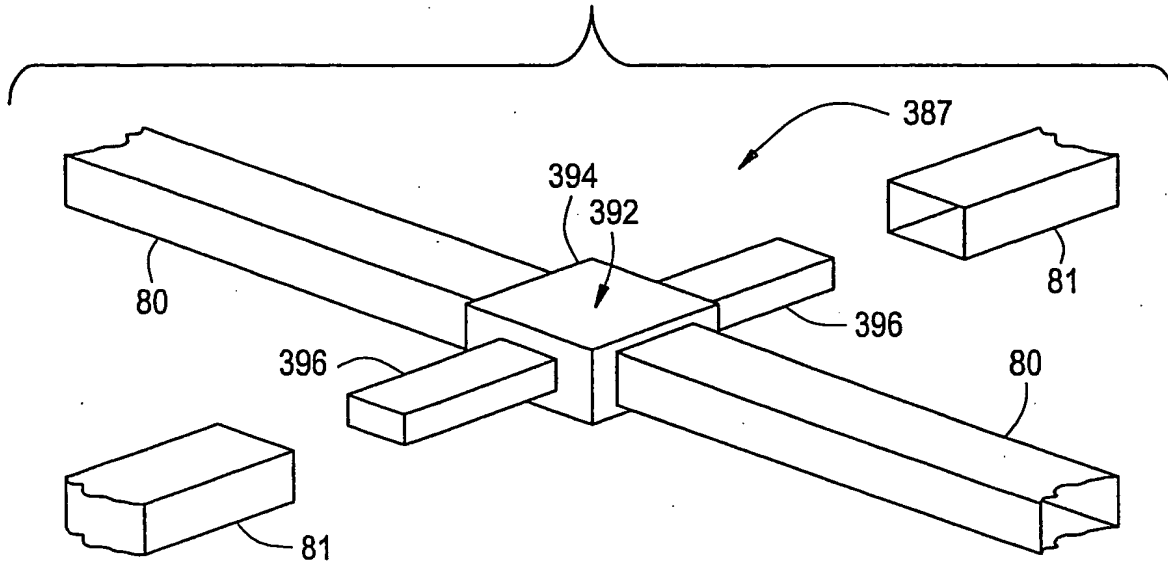
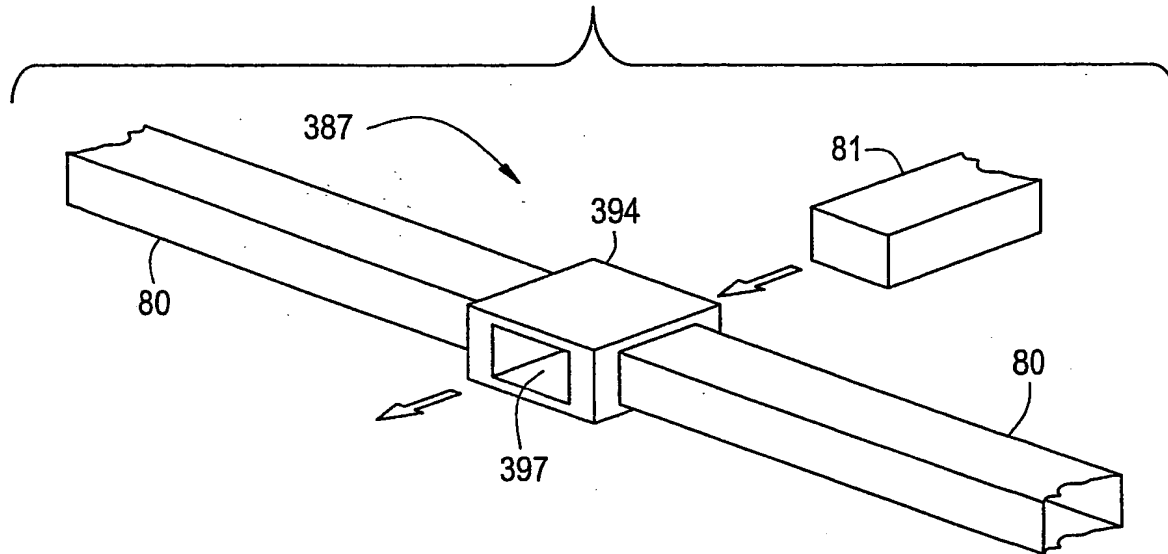


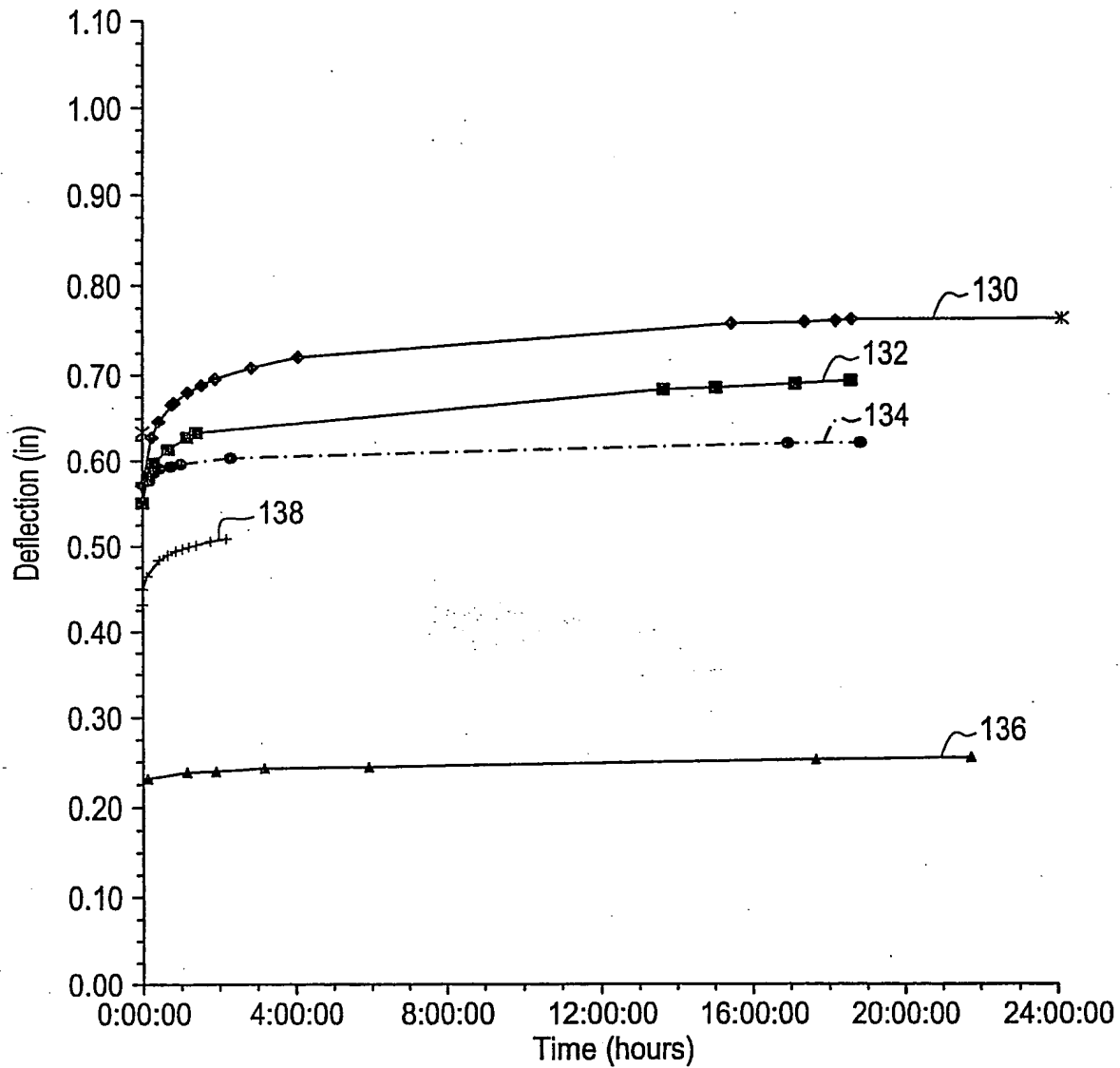
FIG. 26D



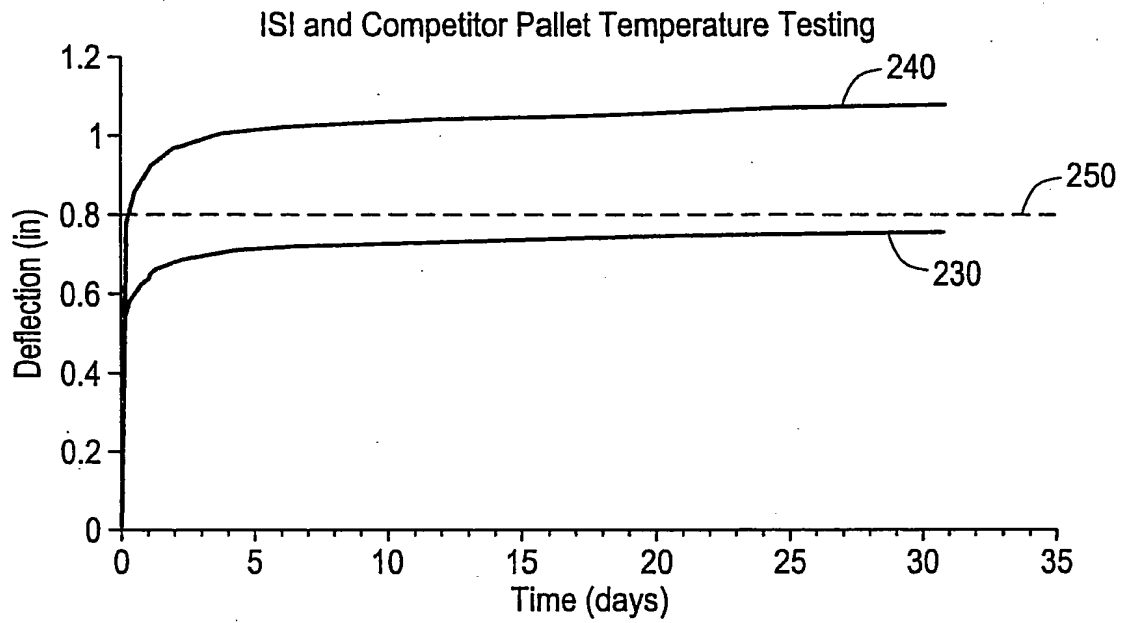


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**FIG. 27**  
Virginia Tech Load Test Summary



**FIG. 28**



**FIG. 29**

